This system realizes high-sensitivity by multiple SDD with high-count rate and high-energy resolution. It is possible by Transistor Reset Processing and Digital Signal Processing.

### Detector
SDD 50mm², Window-less / AP3.3 / Be

### Element area
260mm² (65mm² x 4 element)

### Active area
200mm² (65mm² collimated to 50mm² x 4 element)

### Measurement mode
Histogram / List / Waveform / ROI-SCA

### ADC sampling
4CH 100Msps 14bit

### Energy resolution (typ.)
244eV@5.9keV MnKα
*Peaking time: 0.25μs, 1000kOCR

### SDD power supply
-200 V, ±5V, +3.3V

### Throughput
Max. 150kcpss : 2us
Max. 1000kcpss : 0.15us

### Interface
Ethernet (TCP/IP)

### Option
Z-axis movement mechanism, UHV valve

### Vacuum capable
<10⁻⁵ Pa

### Flange type
ICF114 (Standard)

### Accessory
Software, Instruction Manual

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**XSDD50-04**

10⁻⁵ Pa
Corresponding Vacuum

**Customizable**
Flange type / Tube size

**Recommended Module**

**APN504XGbE**

High Speed ADC
(100MHz・14Bit)

&

Highly Integrated FPGA

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**Detector**

![Detector Image](image)

<table>
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| Flange type    | ICF114 (Standard)                    |
| Accessory      | Software, Instruction Manual         |

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**[Example] Spectrum of soft X-rays**

![Spectrum Image](image)

**[Example] Transport with bellows corresponding to the vacuum**

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*Please note that contents may change without prior notice.

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