Digital Signal Processor

APU8008A / APU8016A

The measurement device features digital signal processing (DSP) capability designed for gamma ray spectroscopy across 16 systems. It directly receives signals from detector preamplifiers, digitizes them using a high-speed ADC (100Msps, 16-bit), applies trapezoidal filtering via FPGA, captures pulse peak values, and generates spectra. Measurement data is transmitted to a computer via Gigabit Ethernet. It maintains list-mode time accuracy consistently, enabling continuous operation suitable for extensive measurements. Additionally, it now includes updated spectral analysis software as a standard feature.



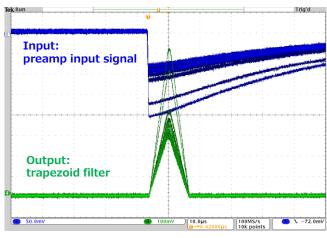
Features

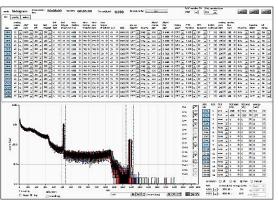
APU8016A

Suitable Detectors	Semiconductor Detector such as Ge, CdTe, Si etc. Scintillator Detector such as LaBr3(Ce), NaI(Tl) etc.
Energy Resolution	1.6∼2.2keV@1.33MeV, Ge Semiconductor Detector
Throughput	> 200kcps
Integral Non-linearity	< ±0.025% (typ.)
Differential Non- linearity	< ±1.0% (typ.)
Mode	Histogram/List/Wave Reading
Spectrum Analysis Software	Gauss Fit Analysis, Peak Search Analysis, Dead Time Adjustment, Energy Correction, Half Width Correction

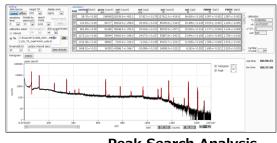


8/16 ch, LEMO Connector Rang: $\pm 2V$, Input Impedance: $1k\Omega$
Coarse Gain x1, x2, x5, x10
100Msps, 16bit
16k, 8k, 4k, 2k, 1k, 512, 256 ch.
Trapezoidal Filter Rise time: $0.1\sim20\mu s$ ($0.01\mu s$ step) Flattop time: $0.05\sim2\mu s$ ($0.01\mu s$ step) Timing Filter, Baseliner Restorer, Pileup Rejecter, Auto-pole zero, Auto-threshold, etc.
Coarse Gain x1,x2,x4,x8,x16,x32,x64,x128 Fine Gain x0.3333~x1.0000
GATE Input, VETO Input, Clock Input, Clear Input, etc. LEMO Connector
Gigabit Ethernet, TCP/IP, UDP
AC100V(0.6A max)~240V(0.3A max), 50/60Hz
300(W)x56(H)x335(D) mm *attachment excluded, approximate 3100g
Data Measurement Control, Spectrum Analysis Software





Histogram Mode



Peak Search Analysis

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^{*}Images is for illustration purpose.

^{*}Please note that contents may change without prior notice.