

APV2200

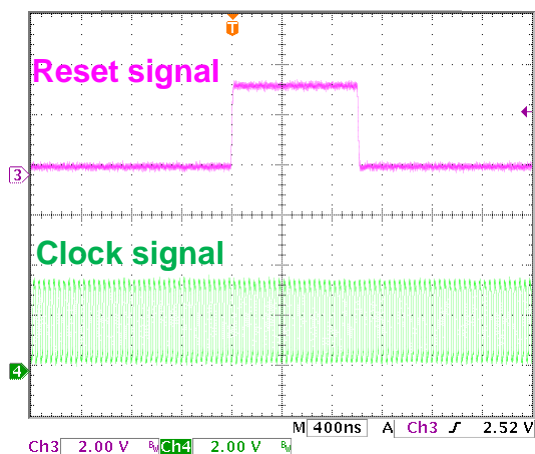
APV2200 is a digital I/O board which is clock/reset signal generator of **32 channel** at the maximum is possible.

It is most suitable for the synchronization between a lot of boards due to common clock/reset signal.

The signal output of the TTL level is possible with all channel.

Additionally, the reset signal output is possible due to **remote control via Ethernet** or manual operated timing by the switch of front panel.

The allocation of the clock/reset function is settable individually by all channel.



Example output signal waveform



Specification

Channel	32 at the maximum
Output signal	LV-TTL level
Clock output	25 MHz *customizable Duty ratio: 50%
Reset output	Pulse width: About 1us *customizable
Allocation of output signal	Choose clock or reset on every channel at the order *customizable
Connector for output	HIF3BA-34PA-2.54DS
Communication I/F	TCP/IP 100BaseT-standard, RJ45
External dimensions	VME 6U 1 width 20(W) x 262(H) x 187(D) Unit: mm
Weight	About 300 g
Environmental condition	Operating temperature: 0 - 40 degrees, No dew condensation



Extension cable *option
HIF-LEMO 16ch. conversion

*Images is for illustration purpose.
*Please note that contents may change without prior notice.

