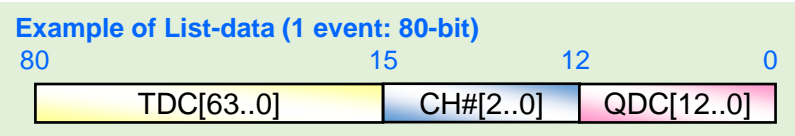
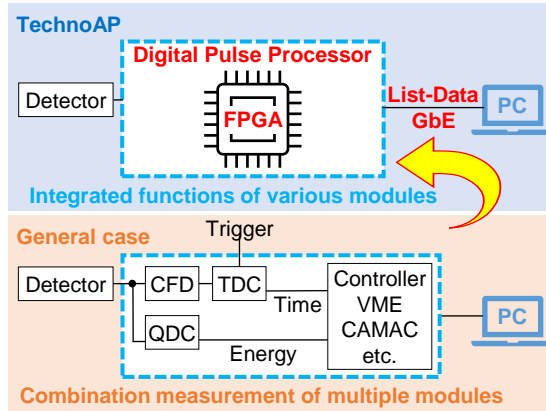




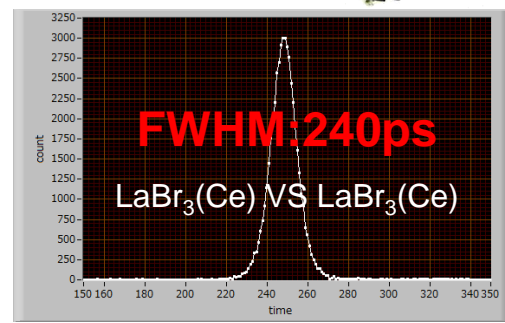
This is a waveform analysis board equipped with four high-speed and high-resolution ADC (1 GHz, 14-bit) channels. In addition to 1 GHz real-time analysis by FPGA, high-speed processing with no dead time due to signal processing is realized with high time resolution and high throughput by adopting Gigabit Ethernet communication. All ADCs operate synchronously at 1 GHz clock and can also be used for signal analysis from multiple fast scintillation detectors. It also supports synchronous processing between multiple boards, and it is easy to extend it to multi-channel analysis.

### Features

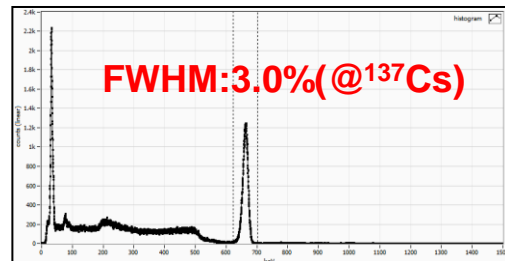
- ADC
  - 1 GHz, 4 ch., Resolution: 14-bit, Synchronous
- Time Resolution
  - Coarse: 1 ns | Fine: 3.9 ps**
- Output
  - 1 Mcps and more / channel
- Mode
  - List (TDC+QDC etc.) Waveform acquisition etc.
- Functions
  - (digital)CFD, TDC, DC, PSD, **List-wave\***, **LW-pileup\***
  - \*Option



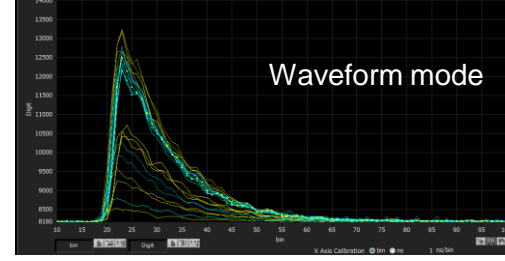
Analog signal input type	Anode signal of PMT, Fast-NIM signal etc.
Analog input range	±1 V, input impedance: 50 ohm, LEMO connector * Customizable up to ± 4 V
Analog offset adjustment	±2V (12-bit) * Customizable from ±20 mV up to ±4V
Analog gain switch	×1, ×3 * Customizable up to × 10
Analog signal risetime	1 ns or less (gain: x 1)
External terminal of input and output (TTL level)	CLK input, CLK output, GATE input, VETO input, CLR input, OR output, (LEMO connector x 6) * I/O signal customizable
Communication I/F	Ethernet (TCP / IP) 100BASE-TX, 1000BASE-T*
Dimensions	20mm (W) × 262mm (H) × 187mm (D), VME 6U
Weight	Approx. 430g
Power consumption	+5 V (approx. 2.9A), +12 V (approx. 0.7A), -12V (approx. 0.3A)



Time Resolution



Energy Resolution



\*Images is for illustration purpose.  
\*Please note that contents may change without prior notice.

